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**To: Users of ANSI/CEA-805-D Data Services on the Component Video Interfaces**

**From:** CEA Technology & Standards Department

**Date:** January 29, 2010

**Subject:** Errata

Please note that there was an error in the published version of ANSI/CEA-805-D (November 2008). The following correction has been provided. We apologize for the inconvenience.

At the time of the addition of the Type B packet to this standard, the bit order chosen for Type B packets was opposite the bit order for Type A packets.

1. Section 4.5 is revised as follows:

#### 4.5 Data Payload Descriptions

Section 4.4.1 describes the payload waveform characteristics for both Type A and B packets. Section 4.4.2 describes the payload data for Type A packets. Section 4.4.3 describes the payload data for Type B packets. Section 4.4.1 defines transmission rules for data payloads.

CEA-805-D compliant sink devices shall recognize and process Type B packets (identified with header value 50). In addition, sink devices shall be backward compatible with CEA-805 source devices, and hence recognize and process Type A packets. Also, when processing Type A packet waveforms, sink devices shall recognize whether h0, h1 is other than 1,1, and if so, sink devices shall process the data according to IEC 61880 or 61880-2 (as appropriate). Support for Type A Packet data in source devices is deprecated.

Note: At the time of the introduction of the Type B packet, in this standard, the bit order opposite the one for Type A packets was chosen for Type B packets. Specifically, for Type A packets the bits are transmitted in the order shown in the Table for the Payload Data for Type A Packets (Table 9). For Type B packets the bits are transmitted in the order shown in the Table for Type B Packet Payload Data (Table 12). This is not a typographical error and is intentional. Both packets are internally consistent in the portions of 805 that describe each packet.